

Receipt

03500.016294.1

PATENT APPLICATION



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

YASUO SUDA

Application No.: 10/630,680

Filed: July 31, 2003

For: SEMICONDUCTOR DEVICE
AND ITS MANUFACTURE
METHOD

)
:
) Examiner: T.T. Nguyen

)
:
) Group Art Unit: 2812

)
:
)
:
) November 10, 2003

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

REQUEST FOR CORRECTED FILING RECEIPT

Sir:

Applicant's attorneys have received an official Filing Receipt in the above-identified application in which the address of the Assignee is incorrect. The address of the Assignee should read as follows:

--TOKYO, JAPAN--.

Issuance of a corrected Filing Receipt, corrected as shown above, is accordingly respectfully requested.

Applicant's undersigned attorney may be reached in our New York office by telephone at (212) 218-2100. All correspondence should continue to be directed to our address given below.

Respectfully submitted,



A handwritten signature in dark ink, appearing to read "Mald C. C. C.", written over a horizontal line.

Attorney for Applicant

Registration No.

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APPL NO.	FILING OR 371 (c) DATE	ART UNIT	FIL FEE REC'D	ATTY. DOCKET NO	DRAWINGS	TOT CLMS	IND CLMS
10/630,680	07/31/2003	2812	750	03500.016294.1	17	11	2

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 FITZPATRICK CELLA HARPER & SCINTO
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DEV

CONFIRMATION NO. 7671

FILING RECEIPT



OC000000011124451

Date Mailed: 10/28/2003

Receipt is acknowledged of this regular Patent Application. It will be considered in its order and you will be notified as to the results of the examination. Be sure to provide the U.S. APPLICATION NUMBER, FILING DATE, NAME OF APPLICANT, and TITLE OF INVENTION when inquiring about this application. Fees transmitted by check or draft are subject to collection. Please verify the accuracy of the data presented on this receipt. **If an error is noted on this Filing Receipt, please write to the Office of Initial Patent Examination's Filing Receipt Corrections, facsimile number 703-746-9195. Please provide a copy of this Filing Receipt with the changes noted thereon. If you received a "Notice to File Missing Parts" for this application, please submit any corrections to this Filing Receipt with your reply to the Notice. When the USPTO processes the reply to the Notice, the USPTO will generate another Filing Receipt incorporating the requested corrections (if appropriate).**

Applicant(s)

Yasuo Suda, Kanagawa, JAPAN;

Assignment For Published Patent Application

CANON KABUSHIKI KAISHA, Ohta-ku, JAPAN;

Domestic Priority data as claimed by applicant

This application is a DIV of 10/099,960 03/19/2002 PAT 6,635,941

Foreign Applications

JAPAN 2001-081000 03/21/2001

JAPAN 2001-108726 04/06/2001

If Required, Foreign Filing License Granted: 10/27/2003

Projected Publication Date: 02/05/2004

Non-Publication Request: No

Early Publication Request: No

Title

Semiconductor device and its manufacture method